**JARVIS(Jiaye) LU**

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**EDUCATION**

**University of San Diego (USD)** Expected Graduation: May 2021

BS/BA in Engineering, Concentration in Embedded Software GPA: 3.5

**COMPUTER SCIENCE & ENGINEERING RESEARCH PROJECTS**

***Founder & Research Lead*, Capacitor Energy Storage Device**  Fall 2018 - Present

* Created a patent pending device that can extract energy from within capacitors at high efficiency and output flexible power based on load
* Worked closely with Professors and industry experts and deepened the understanding of renewable energy storage and the economics behind it

***Founder & Research Lead*, AI-driven Coding Assistant**  Summer 2020 - Present

* Developed a coding assistant program that allows the user to store and share past coding work. Machine learning is implemented to be trained by the shared code so the program could better assist the coding process and be able to generate code on demand

***Research Lead*,** **Beowulf Cluster, USD** Spring 2019 - Spring 2020

* Conducted multi-threading on a cluster of retired single board computers which resulted in

lower-cost and lower power consumption simulation of advanced fluid dynamics

* Directed the project and provided technological insights in its development

**WORK & LEADERSHIP EXPERIENCE**

***Sales Technician (2015~2018)/ R&D Engineer*, POSTEK Electronics Co. Ltd.** 2015 - Present

* Assisted the development project to meet technical specifications from a world leading smart phone manufacturer and successfully delivered ahead of scheduled
* Helped boosted sales in Shenzhen area by 150%
* Wrote Embedded software code under Vxworks that allowed the products to take advantage of networking protocols such as MDNS or SNMP that simplifies and enhance the user experience

***Xatalyze Team, USD*** Spring 2020

* Gathered a team through giving speeches at various club and organizations to compete in the Fowler Global Social innovation challenge.
* Earned first place at USD finals and was awarded US$3,500 for funding

***Senior Design Team, USD*** Fall 2020 - Present

* Pitched the Capacitor Energy Storage Device project and was selected for the Entrepreneur program
* Team leader of the senior design project

**PENDING PATENTS**

* Capacitor energy converter (Patent #: CN 201911381399.1) – A high efficiency converter that extracts the energy within the capacitor and provide a variable output based upon the load.
* Flexible RFID antenna (Patent #: CN 201910384379.3) – A flexible antenna design for RFID label printers that allows the antenna to follow the label during printing, drastically increasing the printing efficiency and printout quality
* Dual motor cutter design (Patent #: CN 202010771894.X) - This invention was initiated to address the issues with the customer Belle International, a leading Footwear and Sportswear and Apparel Retailer. Cutters designed with this invention has been put into mass production.