**SHAILESH SINGH RAJPUT**

**Mobile:** +91 8861205940 ~ **E-Mail:** shailesh.rajput1988@gmail.com

***Senior Software Development Engineer specializing in object-oriented approaches to platform development using C++. Have experience in full life-cycle of software development processes including requirements gathering, design, coding, testing, debugging. Strengths include:***

***●* Object Oriented Development ● Data Structures**

**● Design Patterns ● STL**

 ***Professional* *Synopsis***

* **Master Of Computer (MCA)** with **over 9 years 7 month’** of experience in Software Development.
* ***Presently associated with Verifone India.***
* A Senior software development engineerwith expertise in spearheading numerous projects and ensuring delivery of projects within the time & cost constraints.
* Technical proficiency in various software development languages, applications and databases.

***Skills Set***

**Technical**

**Languages : C,**C++

**Operating System :** Windows, Linux

**Database :** Sqlite3

**Software Engineering Tool :** GitHub**,** CVS, JIRA

**Methodologies :** Data Structures, Algorithms, OOPS, OOAD, UML,

 Design Patterns, XML

**Frameworks** **:** Qt

***Software Development***

* Understanding the technical & functional specifications.
* Designing, developing, testing, troubleshooting and debugging of the applications.
* Providing post-implementation, enhancement and maintenance support to client for application.

***Academic* *Credentials***

**Master of Computer** from Punjab Technical University in 2013. *Secured 70%.*

 **CDAC** from ACTS Banglore in 2010 ,*Secured B Grade*

**Bachelor of Computer** from Punjab Technical University in 2009. *Secured 70%.*

**HSC** from CGHSC , Bilaspur in 2005. *Secured 74%.*

**SSC** from SSVM, Bilaspur in 2003. *Secured 74%.*

***Organisational*** ***Scan***

**April’19 – Till Date Verifone India, Bangalore**

**Senior Software Engineer P4**

**Project Handles:**

**Title: VIPA Payment Application (EMEA region)**

**Period:** April’19- Till Date

**OS:** Window,Linux

**Language’s:** C,C++,Python, STL, Design Pattern

**Tools:** Eclipse,JIRA, SourceTree, Git, WingIDE, BitBucket

**Project Details:** The VIPA(Payment Application) Interface Specification provides system integrators and retailers with the capability to accept EMV(Euro-Master-Visa) based debit and credit cards using the complete range of fully EMV-certified Verifone smart card reading devices.

**Title: NAVLIB**

**Period**: Nov’19- Till Date

**OS:** Window,Linux

**Language’s:** C,C++,Python, STL,Design Pattern

**Tools:** Eclipse,JIRA,SourceTree,Git,WingIDE,BitBucket

**Project Details:** The purpose of Navigator feature is to add PIN entry capability to a touch screen device,

 allowing PIN entry to be performed by a visually impaired person on the touch screen. The visually impaired require some special features such as a highly visible keypad, audible cues and

 confirmation. Achieving this feature will be a first for VeriFone allowing more prolific use of touch

 devices as PIN entry devices

**Role & responsibilities:**

(1) Owning VIPA products, components, complex features including designing new features, implementation and maintenance

(2) Deliver the assigned solution / components on time. Ensure Quality of grooming in terms of the acceptance criteria  and tasks, ensure adequate test cases are documented.

(3) Active contribution to Feature change requests reviews  & PR reviews.

(4) Coordination of others developers, working on the same project.

(5) Customer support, direct contact with customers.

(6) Api’s integration (through different protocol/module) and involved into unit testing.

**Sept’16 – April’19 Graphene Semiconductor Pvt. Ltd., Bangalore Senior Technical Staff member**

**Project Handles:**

**Title: CVFe (Convergent Validation Framework)**

**Period:** Aug 2017 - Feb 2018

**OS:** Window

**Language’s:** C,C++,Python, STL,Design Pattern

**Tools:** MSVC,JIRA,SourceTree,Git,WingIDE

**Project Details:** NVM Express (NVMe) or Non-Volatile Memory Host Controller Interface Specification (NVMHCIS) is an open logical device interface specification for accessing non-volatile storage media attached via a PCI Express (PCIe) bus.

**Role & responsibilities:**

(1) Designing and developing Test Automation Framework (C++/Python) based on the requirements received from Firmware Development and Test Validation Team.

(2) To support CVF issues that arises while regression testing is being conducted.

(3) Writing Test Scripts and libraries in python to automate testing of new features.

(4)Gaining knowledge on NVMe installation procedure and understanding the different part on application.

(5) Implemented different types of RESET features .

(6) Added Json Parsing logic.

(7) Implemented Thread synchronization.

**Project Handles:**

**Title: CTF (Core Testing Framework)**

**Period:** From Sept’2016

**OS:** Window

**Language’s:** C, C++, Python, STL,Design Pattern

**Tools:** MSVC,JIRA,SourceTree,Git,WingIDE

**Architecture**: Client/server Architecture

**Project Details:** SanDisk is an American manufacturer of flash memory products, including memory cards and readers, USB flash drives, and solid state drives. As of February 2015, it is third-largest manufacturer of flash memory. In SanDisk, am part of CTF (Core Testing Framework) Development.
 Core Test Framework (CTF) is a set of libraries with interfaces in Python, C++ and C#. Test scripts and Applications interact with the Device Under Test (DUT) through the easy to use APIs provided by CTF. CTF Supports real devices (ex: SD Card, USB drive, SATA flash drive, etc.) as well as simulated devices (with Livet Model). CTF is used by Firmware Validation teams, Firmware Development teams, and Systems teams, DVT and Characterization teams and almost all test teams in SanDisk.

**Role & Responsibilities:**

 (1) Designing and developing Test Automation Framework (C++/Python) based on the requirements received from Firmware Development and Test Validation Team.

(2) Delivering CTF requirements on time for the Lambeau Program (SD Protocol).

(3) To support CTF issues that arises while regression testing is being conducted.

(4) Implanting FPGA dump log parsing Feature in C++/Python.

(5) Writing Test Scripts and libraries in python to automate testing of new features.

(6)Gaining knowledge on SD Device Driver installation procedure and applying the same.

**Feb’14 – Sept’16** [**Acrowit Technologies Pvt. Ltd.**](http://corporatedir.com/company/acrowit-technologies-private-limited) **, Bangalore SeniorSoftware Engineer**

**Projects Handles:**

**Title:** WittyParrot

**Period:** From Apr’ 2014

**OS:** Window,Mac,Linux

**Languages: C++,** STL,Design Pattern

**Tools:** Qt, JIRA,Git,SourceTree QtCreator

**Architecture:** Model-View-Controller Architecture

**Project Details:** Witty parrot is a cloud based content delivery platform that helps companies and individuals to “achieve speed and consistency” in all of their communication. Witty parrot helps to amplify the three influencing factors (speed, consistency and relevance). Which manages content in the reusable object called Wit.

**Roles and Responsibilities:**

**(1)**As a **Senior Software Engineer**, responsible for designing of application and involved in coordination with the development team.

(2) Work on tools UI design using QT Framework.

(3) Involved in feature discussion and feature prioritization.

(4) Api’s integration (through https/http protocol) and small part of unit testing.

(5) Designed and developed UI screens, such as Tree Widget, List Widget, Buttons, and wrote libraries.

(6) Solve Memory leak issues in Tools/Library.

(7) Worked on solving issues related to the application.

**Projects Handles:**

**Title:** WittyParrot Microsoft Add-ins

**Period:** From Nov 2015 – Jan-2016

**OS:** Window/Linux

**Languages: C#,JavaScript**

**Tools:** Visual Studio

**Project Details:** Through Wittyparrot outlook-addin , we exposing wittyparrot content’s on Microsoft product using their SDK (Visual-Studio Framework), so user does not need to install any external application on system and can access application through any of the Microsoft product(outlook,excel,word ).

**June’12 – Oct’13 Altimetrik India Pvt Ltd, Bangalore Software Engineer**

**Projects Handled:**

**Title:** AltiPay

**Period:** From June’ 12 – Oct’ 13

**OS:** Linux (Fedora)

**Languages: C, C++,** STL,Design Pattern

**Tools:** Qt, Vi Editor, CVS

**Architecture:** Three Tyre Architecture

**Project Details:** Financial Inclusion is the process of ensuring access to appropriate financial products and services needed by vulnerable groups such as weaker sections and low income groups at an affordable cost in a fair and transparent manner by mainstream Institutional players. Delivery of financial services at an affordable cost to vast sections of disadvantaged and low income groups.

 FI solutions assist financial institutions to meet the empowering unbanked customers by providing them with access to mobile banking services and collaborates with its clients to reach unbanked customers and enable convenience mobile banking for their banked customers. FI is designed to be used by the agents for enrolling the customer's personal and biometric information and for performing secure transactions.Developed Software for ingenico POS device

**Role & Responsibilities:**

**(1)** In **Altimetrik India Pvt. Ltd.** I worked as an Individual Contributor. So thoroughly understand business requirements and transform them to design.

(2) Developed the code for the project based on design documents.

(3) Integrated 3rdparty library with application. Biometric verification Implementation.

(4) EMV card reading and writing through application.

(5) Worked on solving issues related to the application.

(6) Api’s integration (through https/http protocol) and small part of unit testing.

**Sept ’10 - 28th May’12 Verismo Networks Pvt. Ltd., Bangalore Software Engineer**

**Projects Handled:**

**Title: Verismo's VuNow™ Internet TV**

**Period:** Sept’ 10 – May’ 12

**OS:** Linux

**Languages:** **C++,XML**

**Tools:** Vi Editor

**Project Details:** **Verismo Networks Pvt. Ltd.** mission is to provide an end-to-end solution for the easy deployment of Broadband entertainment services by ISPs, Telcos, OTT and IPTV service providers globally. Our Broadband TV platform is the leading solution that brings seamless integration of IPTV linear channels, video on demand, internet video, social networking and personal media playback directly to the TV.

**Roles & Responsibilities:**

**(1)**In **Verismo Networks Pvt. Ltd.** I worked in STB domain, with Product development team and Involved actively in Requirement Analysis, Designing, Coding, Api integration (through https/http protocol) and System Testing of STB (Set Top Box).

(2) Worked on solving issues related to the application.

(3) Developed the code for the project based on design documents.

(4) Work on tools UI design.

**Projects Handled:**

**Title: Roku Application**

**Period:** Jan’ 11 – May’ 11

**OS:** Linux

**Languages:** **C++, XML**

**Tools:** Vi Editor

**Project Details:** The Roku Streaming Player (or simply Roku) is a series of [digital media player](https://en.wikipedia.org/wiki/Digital_media_player) [set-top boxes](https://en.wikipedia.org/wiki/Set-top_box) manufactured by [Roku, Inc.](https://en.wikipedia.org/wiki/Roku%2C_Inc.) Roku partners provide [over-the-top content](https://en.wikipedia.org/wiki/Over-the-top_content) in the form of channels. A Roku streaming device gets data (the video stream) via a wired or [Wi-Fi](https://en.wikipedia.org/wiki/Wi-Fi) connection to an Internet router. The data is output via an audio cable, video cable or both signals over an [HDMI](https://en.wikipedia.org/wiki/HDMI) cable. The device can be connected to any television set (or other device) with appropriate input connections.

**Roles & Responsibilities:**

**(1)** In **Roku Application** Involved in every part of application (front-end and back-end) development and their interfacing through events.

(2) I also involved in review process and executed test cases and maintain reports and prepared knowledge transfer documents.

(3) Developed the code for the project based on design documents.

***Personal Information***

Date of Birth : 21st April, 1988

Address : FF-6, B-block, elegant orchid, 8th Main Road, near Jhanavi Shelters,

Akshayanagar, Bangalore-560068

Linguistics : English, Hindi