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Shree Shourya NR

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| Objective | As an Engineer, I have gained experience in making the most of my resources. I have realized the importance of analyzing and optimizing real-life scenarios. Hence, I wish to bridge the gap between research and industry by working in an organization with a similar perspective. |
| Skills | **Cadence tools**  (**PCB Toolkit**) - PSpice®, PSpice®-AA, Allegro® PCB Editor, Allegro® System Capture, Allegro® Sigrity SI, Allegro® Sigrity PI, Virtuoso® Schematic Editor, Pegasus, Quantus™ Extraction.  (**CAS Toolkit**) - Solution, Spectre® AMS Designer, Spectre® AMS Connector, SKILL Language Programming, AWR Microwave Office  **Analytical skills**  C, Python, Verilog, Verilog-AMS, PHP, SQL, R-Programming, MATLAB, AutoCAD, PSCAD, ETAP, Altair Flux™, Simulink, Linux  **Visualization skills**  Excel, Tableau, Power-point, Access, Publisher, Camtasia, Snagit, Hive |
| Experience | INTERN APPLICATION ENGINEER (CAS AND SPB GROUP), Cadence Design SystemsBANGALORE, karnataka (Aug’20 - Present)  SPB TEAM   * Created and updated training lectures, labs, exams, and demos aligned with software releases and with high levels of quality. * Worked extensively on PCB layout solution tool (Allegro® PCB Designer) and with the SCORM-compliant Learning Management Systems (LMS) * Created videos using screen capture software, edited and published the video on to a streaming media server * Experienced in producing training content for online training, virtual classroom, and live training delivery formats and translating courses to other languages like Mandarin   CAS TEAM   * Worked on Circuit Design, Simulation, and Layout Verification tools (Virtuoso® Schematic Editor, Quantus™ Extraction Solution, Spectre®, Pegasus™) for Educational Services training division. * Customer training and education experience with the Cadence Virtuoso tool suites. * Managed development of multiple Cadence tools training at the same time and worked on designs to demonstrate the tool’s features for training.  Intern EMBEDDED ENGINEER, INVENTERON tECHNOLOGIESBangalore, karnataka (Jun’19 - Aug’19)   * Received training in the department of embedded systems and IoT. * In a team of 8, developed a wending machine with e-money, using Raspberry-Pi, Python, and HTML. * Worked with PHP to build testable and reusable PHP modules. * Learned the importance of teamwork and effective communication.  PROJECT MANAGER, team chimeraBangalore, karnataka (Jun’17 - Aug’19)   * Was part of the Business presentation at Formula Hybrid (2018 New Hampshire, USA) * Worked closely with technical department to create and maintain marketing materials for sales presentations and client meetings. * Revitalized web content and generate annual reports using Tableau. * Masterminded highly successful digital strategies for various platforms to attract investors |
| Education | **Bachelor of Technology (Electrical and Electronics Engineering)**, Rashtreeya Vidyalaya College of Engineering (RVCE), Visvesvaraya Technological University (VTU), Bangalore, Karnataka; CGPA-7.5/10 (2016-2020)  **Class XII (Science Education),** Expert Pre-University College, Mangalore, Karnataka: 89/100 (2014-2016) |
| Certifications | * Google Data Analytics (Jun 2021) * 5G mm-Wave course, Cadence Design Systems (Feb 2021) * SKILL Development of Parameterized Cells, Cadence Design Systems (Jun 2021) * Verilog Language and Application, Cadence Design Systems (May 2021) * Virtuoso® Layout Pro: T5 Interactive Routing, Cadence Design Systems (Dec 2020) * Allegro Design Entry HDL Front-to-Back Flow, Cadence Design Systems (Dec 2020) * Allegro System Capture, Cadence Design Systems (Dec 2020) * SKILL Language Programming, Cadence Design Systems (Nov 2020) * Virtuoso® Schematic Editor, Cadence Design Systems (Aug 2020) * Managing Project Risks, University of California (Jun 2020) * Initiating and Planning Projects, University of California (Jun 2020) * Budgeting and Scheduling Projects, University of California (Jul 2020) |
| PUBLICATIONS AND PRESENTATIONS | * Was part of the Business presentation at Formula Hybrid (2020 New Hampshire, USA) * Presented a paper on ‘Design of nano-grid based induction heating cooktop with the analysis of Power Factor Controller (PFC) rectifiers’, RVCE (Mar’20) * Published and presented a paper on ‘Depression detection using facial features’ in the International Journal of Engineering Research and Technology (Apr’20) * Published and presented a paper on ‘Automated Blood Booking System’ in the Department of Information Science at SJBIT College of Engineering (Dec’19) |
| declaration | I hereby declare that the information given above is correct to the best of my knowledge.      (Shree Shourya NR) |
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