**Name: Bhavana Gatla Email id:** bhavana.gatla24@gmail.com

 **Mobile No:** +91-7981566939

**CAREER OBJECTIVE:**

To pursue a challenging career involving my technical, analytical, and interpersonal skills to give an outstanding performance to the organization with utmost dedication and commitment to the mission and vision of the team and the organization.

**ACADEMIC QUALIFICATIONS:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Course** |  **Institution** | **Board/****University** | **Year of Completion** | **Percentage** |
| B. Tech (Electronics And Communication Engineering) | TKR College of Engineering and technology, Hyderabad. | JNTU | 2020 | **65%** |
| Intermediate(MPC) | SR Junior College | Board of Intermediate Education | 2016 | **90%** |
| Secondary School | SR Digi School | Board of Secondary Education | 2014 | **88%** |

**TECHNICAL SKILLS:**

* Programming Languages : C, JAVA
* Operating System : Windows Operating System

**MINI PROJECT & INTERNSHIP**

**Organization: RCI (Research Centre Imarat)**

**Title :** MIL-STD-1553

**Description: Communication between systems :** MIL-STD-1553 is a military standard published by the United States Department of Defense that defines the mechanical, electrical, and functional characteristics of a serial data bus

**MAJOR PROJECT**

**Organization: RCI (Research Centre Imarat)**

**Title :** IMPLEMENTATION OF SPI PROTOCOL ON FPGA

**Description:** Serial Peripheral Interface (SPI)protocol is implemented in Field Programmab le Gate Array (FPGA).Both the components SPI Master and SPI Slave are implemented using state machine diagram.The coding is done in Very higsh speed integrated circuits Hardware Descriptive Language (VHDL).The simulated data is shown and Received data is analyzed by receiving an acknowledgment.

**WORKSHOPS ATTENDED:**

* Attended a workshop on “**PCB DESIGN AND FABRICATION”** under industry-oriented skills development program.
* VLSI “**Very Large-Scale Integration”** Very-large-scale integration is the process of creating an integrated circuit by combining millions of transistors or devices into a single chip.

**STRENGTHS:**

* Adaptable to any environment
* Communication skills

**PERSONAL DETAILS:**

Name : Bhavana Gatla

Date of Birth : 24-10-1998

Residential Address : H. No: 8-90/28/9, Dwaraka Nagar,

Phase -2, Boduppal, Hyderabad- 500 092

Languages Known : English, Telugu and Hindi

Hobbies : Playing Shuttle, Listening Music

**Declaration**

I hereby declare that the above written particulars are true to the best of my knowledge and belief.

**Bhavana Gatla**