



SEERAT

Khanyar, Srinagar (J&K) - 190003

P : 7006771085
E : Seexan28jan@gmail.com
in : www.linkedin.com/in/seeratsofi

OBJECTIVE

A hardworking and ambitious person, curious in solving problems and learning new things. Passionate to learn new technologies and to excel in technological application. Acquiring a challenging position that will enable me to learn, create, innovate and contribute to short and long term goals.

EXPERIENCE

GOVERNMENT POLYTECHNIC COLLEE, GANDERBAL

01/2017 - 06/2017

Guest Faculty

EDUCATION

GOVERNMENT POLYTECHNIC COLLEGE, GANDERBAL

2016

Diploma in Electrical Engineering
Grades - 78

RAMAIAH INSTITUTE OF TECHNOLOGY, BANGALORE

2020

B.E (Electrical and Eldctronics Engineering)
Grades - 7

SKILLS

Programming Language: Proficient in C, Beginner in Python and C++, Proficient in Matlab, Simulink, Microsoft Office, Labview, AutoCad, PScad. ,

TRAININGS AND INTERSHIPS

Six week Internship at Jammu and Kashmir Power Development Cooperation, Bemina, Srinagar. - 2018
Six week Industrial training at Waganpora grid, Srinagar - 2016
Content Writer for Srinagar based Magazine, "Captured Illussion" - 2017
Columnist for newspaper, "Srinagar News" - 2009

PROJECTS

LED CUBE USING NI MDAQ

A hobby project to create multiple patterns using myDAQ. There were 20 control pins for LED cube and 8 pins for Electric Control Circuit which was controlled using single port of myDAQ. The software implementation of the project was done on **LabVIEW**.

SIMULATION ON IMPROVING POWER FACTOR.

Reducing Displacement Power Factor using FC-TCR and Total Harmonic Distortion using suitable filters. Simulation was carried on **Matlab -Simulink**.
(Power factor Improvement using Fixed Capacitor- Thyristor Controlled Reactor (FC-TCR) Reactive Power Compensator)

PROTECTION OF RENEWABLE DOMINATED GRID USING ENERGY STORAGE DEVICES.

The proposed work is to simulate a PV dominated grid in the **PSCAD software** which includes integration of PV array into the grid, simulation of fault on the grid and use Energy Storage Systems to inject negative sequence currents into the grid when the fault occurs for the successful operation of these protection devices.
However, this project remained half done due to Covid-19 emergency.

INTERESTS

Power System, Renewable Dominated Grid, Artificial Intelligence, Digital Communication, Power Electronics, Technical Writing, Machine Learning

LANGUAGE

English, Urdu, Hindi, Kashmiri

ACHIEVEMENTS & AWARDS

Awarded Merit based scholarship for undergraduate studies for the year 2017-2020 (JK PMSSS, Rank 29)
Awarded Best Student award for curricukar and co-curricular activities for the academic session (2013-2016)

PUBLICATION

AN ANALYSIS ON THE SELECTION OF FPGA'S FOR THE DEVELOPMENT OF HIGH SPEED COMPUTING SYSTEMS AT THE HIGH ENERGY PHYSICS EXPERIMENT (ACTSE -2020)

EXTRA CURRICULAR ACTIVITIES

Organised Writing Competition in College Fest - Udhbav'2019
Participated in discussion on "Drug Menace" organised by Doordarshan Kendra, Srinagar - 2010
Hosted Jashn-e-Chillai Kalaan (2016) organised by Funtoosh.
Read poetry in Electra (2018, 2019, 2020) organised by Department of Electrical and Electronics Engineering.
Participated in Open Mic organised by WIE, IEEE (RIT)

HOBBIES

Reading
Writing
Debates
Blogging
Dance
