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Educational Qualification

Examination	School /College	Board/university	Year	Percentage
M.B.A.	SSMV	CSVTU	2011	First Class
B.C.A.	SAI MAHAVIDYALAYA	RSU	2009	First Class

Work Experience

Company Name: SeviTech Systems Pvt Ltd, Bangalore Designation: LEAD – Recruiter (Semiconductor Recruitment) Duration: April 2017 - Till Date (Promoted twice in SeviTech)

Role: Working in End-to-End Recruitment for Semiconductor skills.

- Sourcing profiles major through social networking sites like LinkedIn, job portals, internal references, networking, advertisement & job posting.
- > Skill mapping these profiles and conducting the initial technical phone screening.
- > Coordinating for various modes of interviews along with the interviewers & candidates.
- > Organized and conducted Weekly Recruitment drives (general walk-in & scheduled drives).
- Working closely with Engineering heads, Hiring managers for resource planning & understanding business requirements.
- > Experience in handling both bulk & niche skill hiring.
- Maintaining database and excel sheet tracker for sourcing profiles, status of the candidates and other activities.
- Post offer engagements with prospective candidates to address their queries well in time and ensure their joining happens seamlessly meeting the business requirements.
- > Leading the team members and understanding his/her issues and motivating them to achieve their targets.
- Standing out as a Social Networking Recruiter and connecting with the candidates during their Post Offer Follow-ups
- Strong at Leadership roles and good at taking initiatives and challenges.

Positions Recruited:

Physical Design, STA (Static Timing Analysis), DFT (Design for Test), Design & Verification, RTL Design, FPGA Design, Analog Layout, Standard Cell, Circuit Design, Physical Verification

Extra Responsibilities:

Have been involved in doing joining formalities of new joiners and also exit formalities. Have initiated and coordinated cultural activities during annual fest and cultural events.

Company Name: Synapse Techno Design Innovation Pvt Ltd, Bangalore. Designation: Talent Acquisition -Associate Duration: October 2014 - March 2017

Handling multiple location recruitments

- Handled requirements for 1 20 years of experience.
- Partnering with Hiring Manager of different Business Unit for the Recruitment need mainly Sourcing the Talent from Various channels like using Social Networking site more effectively – Linkedin, Facebook and Twitter more effective in getting candidates and through job postings in various sites like Hirist, Talent connect, Careesma, Indeed etc. etc.
- Understanding the requirement and targeting the Talent pool based on need and also building pipeline for upcoming Requirements for different projects.
- Building networks and contacts to generate candidate flow through employee referrals, news groups and forums, Internet resources and other creative approaches.
- > Mapping the target Companies based on skills, Domain.
- Recruiting candidates across the level for building a complete team.
- > Building a network of Semiconductor and Embedded Professionals.
- > To get top talented engineers by head hunting and using LinkedIn recruiter inmails.
- > Every weekend, sending mass mails and openings details to the folks.
- > Interacting with Clients and involved in scheduling's and follow ups to be done.

Positions Recruited:

Software:

Physical layer – WLAN,

Bluetooth Firmware Development, L2/L3 Protocols, LTE Development, MAC, NAS Development

Physical Testing/Device Driver Testing,

Firmware – WLAN (IEEE 802.11)/ Wifi

Storage Domain: SSD Firmware Development, FTL (Flash Translation Layer),

SSD Validation, Firmware Development, Firmware Validation.

C++, Dot Net, Bluetooth, Java, JNI (Java Native Interface), JavaFX, WPA Supplicant.

BIOS Validation, Firmware Testing & other skills etc. etc.

Company Name: Roljobs Technology Services Pvt Ltd - Leaders in Social Media (Formerly known as Roland & Associates) Designation: IT & Social Media Recruiter (Semiconductor & Embedded)

Duration: October 2011 - 4th Aug 2014.

Roles & Responsibilities

- Taking care of IT & Non IT (Semiconductor & Embedded) end to end recruitment life cycle from junior to mid level positions.
- > Carrying out the recruitment process through Job Portals/References/Social media for various positions.
- Client interaction, screening and Short listing resumes as per the client requirement, taking the preliminary interview, monitoring their soft skills, short listing the right candidate.
- > Pre & post interview co-ordination and follow ups with candidates and clients across various levels.
- Preparing and maintaining Tracker Sheets for the entire recruitment process and updating the Database on daily basis

Roles/Skills Sourced for:

Physical Design, STA (Static Timing Analysis), DFT (Design for Test), Design & Verification, RTL Design, FPGA Design, Analog Layout, Standard Cell, Circuit Design, Physical Verification, Logic Design
Java, J2EE, ASP.NET, IOS, Embedded
Physical layer – WLAN,
Bluetooth Firmware Development, L2/L3 Protocols, LTE Development, MAC, NAS Development
Physical Testing/Device Driver Testing,
Firmware – WLAN (IEEE 802.11)/ Wifi

- Received an appreciation award for closing a very senior position Physical Design for Malaysia Team for UST Global in 2019.
- > Received SPOT Award for the contribution in SYNAPSE Design in 2014.
- > Received appreciation mails from client for handling the requirements in Synapse.
- > Participated & Achieved BEST Drama Award in IPTA Drama Competition.
- > Won many prizes in sports like Race 200 Mtrs, Discuss Throw, Relay Race, etc
- Spend leisure time by reading novels and listening to music.
- Languages known English, Hindi and Bhojpuri

PERSONAL SNIPPETS

Date of Birth Nationality & Religion Sex Marital Status : 25th July 1988 : Indian, Hindu : Female : Single

I do hereby declare that all the above statement furnished by me are true & correct to the best of my knowledge & beliefs.

Priyanka Srivastava