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Sai Venkata Manohar Venna

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Phone: +91 81301 29479
Date of Birth: March 21, 1997

Education	**National Institute of Technology,** Delhi	*2018*
	Bachelor of Technology, Electronics and Communication Engineering	
	GPA: 9.28/10.0	
	Narayana Junior College, Hyderabad	*2014*
	12th – Board of Intermediate Education, Andhra Pradesh	
	Percentage: 97.2%	
	D.A.V. Public School, Secunderabad	*2012*
	10th – Central Board of Secondary Education	
	GPA: 9.6/10.0	

Experience

Qualcomm, Hyderabad — *Nov 2019 – Present*

Engineer, System Integration and Test, LTE Test Base Station

- Develop, execute and integrate full-stack comprehensive test plans for features like V2X and User Plane C-IoT on the LTE network side in line with 3GPP specifications
- Automate, bring up and maintain test suites for various LTE standalone features
- Extensive data performance testing along with regular build sanity and regression
- Reproduce, debug by analyzing logs and root cause customer issues
- Develop and support Python based REST API application for LTE-D client automation

Tejas Networks Ltd., Bengaluru — *Aug 2018 – Nov 2019*

Engineer, Product Verification

- Develop, assemble and test various scenarios on multiple optical networking technologies which include SDH, OTN and DWDM
- Create automated scripts and run test suites to verify the product features
- Work with development teams and report both software and hardware defects

Unio Labs Pvt. Ltd., Hyderabad — *Jun 2018 – Aug 2018*

Embedded Development Intern

- Work on projects related to Image Processing and IoT

Claro Energy Pvt. Ltd., Delhi — *Oct 2016 – Jul 2017*

Embedded Development and UX/UI Design Intern

- Develop products based on remote monitoring and control (M & C)
- Design and test schematics related to different peripherals for the PCBs
- Design user interface for both Android and Web applications

Achievements

Academic

- **Director's Gold Medalist** for securing highest CGPA of B.Tech. (E.C.E.) programme (2014–18 Batch) at NIT Delhi
- Secured **All India Rank** of **413** in GATE (EC) 2018 examination

Competitions/Events

- Winner of Innovation & Design Challenge, TerraTechnica'17, NITD
- Runner up in Prelims round of IoT Challenge 2016 conducted by i3indya Technologies at Delhi Technological University

Position of Responsibility

- Executive member – Clairvoyance, Photography & Design Club 2016, NITD
- Head – Design Team, Insignia 2016, the annual literary fest of NITD

Technical Skills

Scripting/Programming – Python, Linux, Basics of Shell Scripting
Software – AVR Studio, Arduino, Adobe Photoshop

Training

Vector Institute, Hyderabad

Course on 8051 Microcontroller — *May 2016 – Jun 2016*

Project Executed: Home Automation using IR Communication

Course on ARM based Microcontroller — *Jun 2016 – Jul 2016*

Project Executed: ATM System Design using ARM

Projects

Academic Projects

- Campus Automation & Attendance System
- Finger Gestured Light Controller