**Sri Harsha pAllipat**

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**SUMMARY**

* 3.7 years of work experience at Deloitte Consulting US-India as Salesforce Developer.
* Bachelors in Electronics and Communication Engineering, Graduated in 2016 among the top 5% in class.



**EDUCATION**

* CVR College of Engineering | Bachelors, Electronics & Communication (Percentage: 87%) Sep 2012 – April 2016



**WORK EXPERIENCE**

***Deloitte, Hyderabad*** (Consultant), March 2017 – Present)

**Project**: Dyson

* Leading the team for day to day development activities and Providing hands on expert level assistance to developers for technical issues.
* Responsible for communication with client, Understanding Business Requirements.
* Developed Apex classes, Triggers, Lightning Components, Integrations (REST) with external systems (Hybris, MuleSoft).
* Handled SIT, UAT phases and supported 10+ production Go-Live.

**Project**: Cardinal Health

* Responsible for 8+ Client Implementations.
* Built automated tool based on VBScript to handle multiple Client Implementations, which reduced delivery time by 60% for all client programs.
* Building Package and Build XML files for deploying metadata using ANT Deployment tool.
* Developed Apex classes, Triggers, Lightning Components and also worked on Configurations, Workflows, Process Builder, Reports and Dashboards, Data Migration.
* Design, build, test and deploy SFDC customizations which may include form layout changes, workflows, validation rules, etc.



**CERTIFICATIONS**

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| * Salesforce Application Architect |
| * Salesforce Platform Developer |
| * Salesforce Administrator ADM 201 |
| * Salesforce Sharing and Visibility Design * Data Architecture and Management Designer * Salesforce Platform App Builder * Salesforce development lifecycle and deployment designer * Oracle Certified Java Developer |

**ACADEMIC PROJECTS**

**Bluetooth Based Home Automation Using –** Embedded C

* Developed a device which was operated through Bluetooth connected to a smart phone via password encryption to enable user to access house hold appliances.
* Designed a circuit which contains 8051 micro controller connected to Bluetooth module and developed the controlling logic code on Embedded C.
* This system is designed to be low cost and scalable allowing a variety of devices to be controlled with minimum changes to its core.

**Design and Synthesis of 64 bit Reversible Logic ALU –** Xilinx, Cadence

* Designed a 64 bit reversible Arithmetic Logic Unit in Verilog using Xilinx, to study the power dissipation and area analysis.
* Calculated power consumption and area (in nm) using Cadence tool, observed that reversible logic dissipate less power and occupies less area in Integrated Circuit compared to traditional logic.



**SKILLS**

* Technologies: Salesforce
* Technical Skills: Java, Python, Apex Classes, Test Classes, Lightning pages (Pages, Component & Controllers), Custom Objects, Validation Rules, Work Flows, Dashboards, Reports
* Tools: Workbench, Jenkins, Ant Migration Tool, Data Loader, Azure DevOps



**ACHIEVEMENTS**

* Received Best Performer award for fiscal year 2017-18.
* Received 4 Spot Awards and 1 Applause Award for best performance.
* Received Accolades from Managing Directors and Senior Managers for outstanding Client Impact.
* Received 5+ Client Appreciations for reducing the overall delivery time of implementation.